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TITLE: COMMUNICATION DEVICE

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COMMUNICATION DEVICE

BACKGROUND OF THE INVENTION

5 1. Field of the Invention

The present invention relates to communication device mounting for example a global positioning system (GPS) in a mobile phone or other portable terminal.

2. Description of the Related Art

10 In a GPS measuring the position of a mobile body utilizing satellites (GPS satellites), the basic function of a GPS receiver is to receive signals from four or more GPS satellites, calculate the position of the receiver from the received signals, and inform it to the user.

15 The GPS receiver demodulates a signal from a GPS satellite to acquire orbital data of the GPS satellite and derives its own three-dimensional position from the orbit and time information of the GPS satellite and delay time of the received signal by simultaneous equations.

20 The reason why four GPS satellites giving received signals are required is that there is error between the time inside the GPS receiver and the time of a satellite and that error must be eliminated.

25 In the case of a GPS receiver for consumer use, a positioning computation is carried out by receiving a

spread-spectrum signal wave referred to as the "L1 band" or "C/A" ("coarse acquisition" or "clear and acquisition") code from a GPS satellite (Navstar).

The C/A code is a signal obtained by binary phase 5 shift keying (BPSK) modulating a carrier wave (hereinafter referred to as a "carrier") having a frequency of 1575.42 MHz by a signal obtained by spreading data of 50 bps by a code of a pseudorandom noise (PN) sequence having a transmission signal rate 10 (chip rate) of 1.023 MHz and a code length of 1023, for example, the Gold code. In this case, since the code length is 1023, the C/A code is formed as a code comprised of the code of the PN sequence repeated using 1023 chips as one cycle (one cycle = 1 millisecond 15 (msec)) as shown in FIG. 1A.

The code of the PN sequence of this C/A code is different for every GPS satellite, but is configured so that which satellite uses which code of the PN sequence can be detected by a GPS receiver in advance. Further, 20 the navigation message mentioned above enables the GPS receiver to learn from which GPS satellites signals can be received at that position and that point of time. Accordingly, in the case of for example three-dimensional positioning, the GPS receiver receives the waves from 25 four or more GPS satellites which can be acquired at that

position and that point of time, despreads the spectrum, and performs the positioning computation to find its own position.

Then, as shown in FIG. 1B, one bit of the satellite signal data is transmitted as 20 cycles' worth of the code of the PN sequence, that is, 20 milliseconds. Namely, the data transmission rate is 50 bps. 1023 chips of one cycle's worth of the code of the PN sequence are inverted between a time when the bit is "1" and a time when the bit is "0".

As shown in FIG. 1C, in a GPS, one word is formed by 30 bits (600 milliseconds). Further, as shown in FIG. 1D, one sub-frame (6 seconds) is formed by 10 words. As shown in FIG. 1E, the word at the header of one sub-frame has a preamble always regarded as a bit pattern even when data is updated inserted into it. The data is transmitted after this preamble.

Further, one frame (30 seconds) is formed by five sub-frames. The navigation message is transmitted in data units of this one frame. First, three sub-frames in this one frame of data form information inherent in the satellite referred to as "ephemeris information". This information includes parameters for finding the orbit of the satellite and a transmission time of a signal from the satellite.

All GPS satellites are provided with atomic clocks and use common time information. The transmission time of a signal from a GPS satellite is a one second unit of the atomic clock. Further, the code of the PN sequence of the 5 GPS satellite is generated as a code in synchronization with the atomic clock.

The orbit information of the ephemeris information is updated every several hours, but becomes the same information until that update. By holding the orbit 10 information of the ephemeris information in the memory of this GPS receiver, however, the same information can be precisely used for a few hours. Note that the transmission time of the signal from a GPS satellite is updated every one second.

15 The navigation message of the remaining two sub-frames of one frame of the data is information commonly transmitted from all satellites and referred to as "almanac information". 25 frames' worth of this almanac information become necessary in order to acquire all 20 information. It is comprised of rough position information of each GPS satellite, information indicating which GPS satellites can be used, etc. This almanac information is updated every several months, but becomes the same information until that update. By holding this 25 almanac information in the memory of the GPS receiver,

the same information can be precisely used for several months.

In order to receive a GPS satellite signal and obtain the above data, first the carrier is removed, then 5 a code of the PN sequence (hereinafter the code of the PN sequence will be referred to as the "PN code") the same as the C/A code used in the GPS satellite to be received prepared in the GPS receiver is used to acquire the signal from the GPS satellite and despread the spectrum.

10 When phase synchronization with the C/A code can be established and the spectrum is despread, the bit is detected, and it becomes possible to acquire a navigation message including the time information from the signal from the GPS satellite.

15 The signal from the GPS satellite is acquired by phase retrieval of the C/A code. In this phase retrieval, the correlation between the PN code of the GPS receiver and the PN code of the received signal from the GPS satellite is detected. For example, when the correlation 20 value of the result of the correlation detection is larger than the value set in advance, it is judged that the two are synchronized. When it is judged that synchronization has not been established, any synchronization technique is used to control the phase of 25 the PN code of the GPS receiver to synchronize it with

the PN code of the received signal.

As mentioned above, a GPS satellite signal is a signal obtained by BPSK modulating a carrier by a signal obtained by spreading data by a spread code. Accordingly, 5 in order for a GPS receiver to receive a GPS satellite signal, it is necessary to establish synchronization of not only the spread code, but also the carrier and the data, but the spread code and the carrier cannot be independently synchronized.

10 A GPS receiver generally converts the carrier frequency in the received signal to an intermediate frequency in several MHz and performs the synchronization detection processing mentioned above by that intermediate frequency signal. The carrier in the intermediate 15 frequency signal includes a frequency error mainly due to a Doppler shift in accordance with the velocity of the GPS satellite and a frequency error of a local oscillator generated inside the GPS receiver when converting the received signal to an intermediate frequency signal.

20 Accordingly, due to these frequency error factors, the carrier frequency in the intermediate frequency signal is unknown, so a frequency search becomes necessary. Further, a synchronization point (synchronization phase) of the spread code in one cycle 25 depends upon the positional relationship between the GPS

receiver and the GPS satellite, so is unknown. Therefore, as mentioned above, some sort of synchronization technique becomes necessary.

The GPS receiver uses a synchronization technique 5 employing a frequency search for the carrier and a sliding correlator + delay locked loop (DLL) + Costas loop. This will be explained below.

The clock for driving the generator of the PN code of the GPS receiver is generally one obtained by dividing 10 the frequency of the oscillation signal of a reference frequency oscillator provided in the GPS receiver. As this reference frequency oscillator, use is made of a high precision quartz oscillator. From the output of this reference frequency oscillator, a local oscillation 15 signal used for converting the received signal from a GPS satellite to an intermediate frequency signal is generated.

FIG. 2 is a view for explaining this frequency search. As shown in FIG. 2, when the frequency of the 20 clock signal for driving the generator of the PN code of the GPS receiver is a certain frequency f_1 , phase retrieval of the PN code is performed, that is, the phase of the PN code is sequentially shifted by one chip, the correlation between the GPS received signal and the PN 25 code at the time of each chip phase is detected, and the

peak value of the correlation is detected, whereby it is possible to detect the phase at which synchronization can be established.

When the frequency of the clock signal is f_1 and 5 there is no synchronized phase in all of the 1023 chips' worth of the phase retrieval, for example the frequency division ratio with respect to the reference frequency oscillator is changed, the frequency of the drive clock signal is changed to f_2 , and 1023 chips' worth of phase 10 retrieval is carried out in the same way as above. This is repeated by stepwise changing the frequency of the drive clock signal as shown in FIG. 2. The above 15 operation constitutes the frequency search.

When this frequency search detects the frequency of 20 the drive clock signal regarded to be able to be synchronized, the final phase synchronization of the PN code is carried out at that clock frequency. Due to this, even if there is deviation in the oscillation frequency of the quartz frequency oscillator, it becomes possible to acquire a satellite signal.

However, if the above-mentioned technique is used as the synchronization method, this would be unsuitable for fast synchronization in principle. In an actual receiver, in order to compensate for this, it would be 25 necessary to search for the synchronization point in

parallel by forming multiple channels. Further, as described above, if a long time is required for the synchronization of the spread code and carrier, the response of the GPS receiver would become slow. This 5 would be inconvenient in usage. Therefore, for the phase synchronization of the spread code, a technique of performing the code synchronization by a digital matched filter using fast Fourier transform (FFT) processing without using the technique of sliding correlation as 10 mentioned above is realized by improvement of the capability of the hardware such as the digital signal processor (DSP).

Summarizing the problems to be solved by the invention, in recent years, products combining mobile phones or other networked portable terminals and GPS 15 receivers and services for the same have been put into practical use. As cheaper networks are built in the future, the network hardware and GPS systems will become even more integrated, so it is predicted that network 20 services utilizing positioning will increase.

The reference frequency oscillator applied to a GPS system basically has a fixed oscillation frequency, but the reference frequency oscillator used in a mobile phone etc. is configured so as to change in frequency according 25 to a change in the base station or other conditions.

Accordingly, if simply configuring a wireless communication device combining a network device and GPS system, it would be necessary to mount two types of reference frequency oscillators. However, this would 5 result in the size of the module becoming larger and would also lead to a cost increase. For such a reason, when configuring a wireless communication device combining a network device and GPS system, it is desirable to make joint use of one reference frequency 10 oscillator, that is, a reference frequency oscillator having a variable frequency as used in mobile phones etc. If simply making joint use of such a variable frequency reference frequency oscillator, however, the following disadvantages would arise.

15 As explained above, the transmission signal referred to as the "L1 band" and "C/A code" of the GPS satellite is a signal obtained by BPSK modulating a carrier of 1575.42 MHz by a signal obtained by directly spreading data of 50 bps by the Gold code having a code 20 length of 1023 and a chip rate of 1.023 MHz. Accordingly, in order to receive a signal from a satellite by a GPS receiver, as mentioned above, it is necessary to establish synchronization between the spread code and the carrier and data, but the synchronization of the spread 25 code and the carrier cannot be independently carried out.

The GPS receiver converts the carrier frequency within several MHz and processes it by the intermediate frequency (IF). However, the carrier at the IF includes an error of the local oscillation frequency mainly 5 generated inside the receiver at the time of the Doppler shift due to the velocity of the satellite and the conversion to IF, therefore the carrier frequency at the IF is unknown. Further, the phase of the spread code depends upon the positional relationship between the GPS 10 receiver and the satellite, so is unknown. Accordingly, when using a reference frequency oscillator changing in frequency according to the conditions, if the frequency changes, the frequency of the IF carrier also changes. If the frequency of the IF carrier changes, the 15 synchronization between the received signal and the spread code can no longer be held. Further, when the frequency is frequently changed, it becomes difficult to establish synchronization with the frequency of a satellite found by the DSP or find out frequency of the 20 satellite.

SUMMARY OF THE INVENTION

An object of the present invention is to provide a communication device not only able to prevent an increase of the module size and cost, but also to reliably find a 25 frequency and reliably establishment synchronization even

if the oscillation frequency of the oscillator changes and able to continuously maintain tracking of the carrier.

To attain the above object, according to a first aspect of the present invention, there is provided a 5 communication device for demodulating a received signal by a spread code, comprising an oscillator for outputting a reference signal having an oscillation frequency changing by exactly a predetermined frequency in accordance with communication conditions; an acquisition 10 unit for performing a search of at least the received signal based on the reference signal of the oscillator; a tracking unit for performing tracking processing of the received signal based on the reference signal of the oscillator; and a control unit for determining a stable 15 time area based on change information of the oscillation frequency of the oscillator and making the acquisition unit perform the search processing in the determined time area.

A communication device according to a second aspect 20 of the present invention has a first communication unit including an oscillator for outputting a reference signal having an oscillation frequency changing by exactly a predetermined frequency in accordance with communication conditions and outputting a frequency change signal when 25 the oscillation frequency of the oscillator is to be

changed and a second communication unit for demodulating a received signal by a spread code, wherein the second communication unit includes an acquisition unit for performing a search of at least the received signal based 5 on the reference signal of the oscillator, a tracking unit for performing tracking processing of the received signal based on the reference signal of the the oscillator, and a control unit for determining a stable time area based on change information of the oscillation 10 frequency of the oscillator and making the acquisition unit perform the search processing in the determined time area.

Preferably, the control unit receives frequency change information and makes the acquisition unit perform 15 the search processing at the frequency before the frequency change after the frequency change of the oscillator.

Preferably, when the frequency changes after the end of the search of the acquisition unit, the 20 acquisition unit gives the information of the change to the tracking unit and makes it perform the tracking processing at a plurality of frequencies obtained by adding the change of the frequency.

According to the present invention, for example, the 25 first communication unit engages in speech with a close

base station or control of transfer of predetermined data in synchronization with a reference signal from an oscillator. The first communication unit changes the oscillation frequency of the oscillator when the base 5 station of the other party in the communication changes according to established protocols by generating a frequency change signal for reporting that the base station of the other party in the communication has changed and frequency is to be changed and outputting it 10 to the control unit of the second communication unit. The control unit of the second communication unit receives the frequency change signal from the first communication unit and, based on the information of the change of the reference frequency, for example, the frequency change Δf 15 or DIR or number of bits or other parameters, performs control so as to continue to maintain the tracking of the carrier in the tracking processing. The acquisition unit uses the frequency change information of the reference signal from the control unit to determine a clock stable 20 area excluding a margin area set in advance in an area where the frequency of the reference signal changes, that is, a clock unstable area, and uses the data at that frequency for a frequency search. Namely, the acquisition unit is controlled so as to reliably find the frequency 25 of the satellite by the control unit using a certain

timewise stable area for the frequency search. Further, the acquisition unit receives the frequency change of the reference signal from the control unit and performs a frequency search of a satellite at the frequency before 5 the frequency change of the reference signal after the frequency change of the reference signal. Namely, the acquisition unit is controlled so as to perform the frequency search of a satellite over a long time by using two or more clock stable areas for the frequency search 10 of the satellite by the control unit. When the frequency of the reference signal changes after the frequency search of the satellite, the acquisition unit establishes synchronization of the frequency of the satellite by transmitting the change $f+\Delta f$ to the tracking unit.

15 BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and features of the present invention will become clearer from the following description of the preferred embodiments given with reference to the attached drawings, wherein:

20 FIGS. 1A to 1E are views of the configuration of a signal from a GPS satellite;

FIG. 2 is a view of an example of synchronization processing of a carrier and a spread code;

25 FIG. 3 is a view of the configuration of an embodiment of a communication device according to the

present invention;

FIG. 4 is a block diagram of an example of the configuration of an acquisition unit according to the present embodiment;

5 FIG. 5 is a block diagram of an example of the configuration of a DSP of the acquisition unit according to the above embodiment;

FIG. 6 is a view for explaining a frequency search of a satellite of the DSP of the acquisition unit;

10 FIG. 7 is a view for explaining the frequency search of a satellite of the DSP of the acquisition unit;

FIG. 8 is a view for explaining the frequency search of a satellite of the DSP of the acquisition unit;

15 FIGS. 9A to 9D are views for explaining processing for removal of a navigation message of the DSP of the acquisition unit;

FIG. 10 is a block diagram showing main components of a tracking unit according to the present embodiment;

20 FIG. 11 is a view of an example of the configuration of the tracking unit when the frequency of a reference signal of a mobile phone unit does not change and a control unit does not receive a frequency change signal;

25 FIG. 12 is a view of an example of the configuration of the tracking unit when the frequency of

the reference signal of the mobile phone unit changes and the control unit receives the frequency change signal;

FIG. 13 is a circuit diagram of an example of the concrete configuration of a loop unit of the tracking 5 unit according to the present embodiment;

FIGS. 14A and 14B are circuit diagrams of an example of the configuration of a low pass filter of a Costas loop of the tracking unit according to the present embodiment;

10 FIG. 15 is a view of characteristics of a phase detector of the Costas loop of the tracking unit according to the present embodiment;

FIGS. 16A to 16C are views for explaining an example of the configuration of a loop filter of the 15 Costas loop of the tracking unit according to the present embodiment;

FIGS. 17A to 17C are views for explaining a phase control operation in a delay locked loop of the tracking unit according to the present embodiment;

20 FIGS. 18A and 18B are views of the characteristics of a phase detector of a delay locked loop of the tracking unit according to the present embodiment;

FIG. 19 is a view for briefly explaining the processing of the control unit according to the present 25 embodiment;

FIG. 20 is a flow chart for briefly explaining the processing with respect to the DSP of the acquisition unit of the control unit according to the present embodiment;

5 FIG. 21 is a flow chart for briefly explaining the processing with respect to the tracking unit of the control unit according to the present embodiment;

10 FIGS. 22A to 22C are views for briefly explaining retrieval of a phase of a PN of the control unit according to the present embodiment; and

15 FIGS. 23A and 23B are views for explaining a decoding function of a navigation message (one word) of the control unit according to the present embodiment.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

20 Preferred embodiments of the present invention will be described in detail below while referring to the attached figures.

25 FIG. 3 is a view of the configuration of an embodiment of a communication device according to the present invention. This communication device is configured as a communication device combining a mobile phone as a networked portable terminal and a GPS receiver and making common use of the reference frequency oscillator of the mobile set.

25 The communication device 1 has, as shown in FIG. 3,

a mobile phone unit 2, a GPS front end unit (GPSFE) 3, and a GPS baseband unit (GPSBB) 4 as main components. Note that a first communication unit is configured by the mobile phone unit 2, while a second communication unit is 5 configured by the GPS front end unit 3 and the GPS baseband unit 4.

The mobile phone unit 2 has mobile phone functions which can be applied to a mobile communication system, for example, a cellular system. The mobile phone unit 2 10 has, as shown in FIG. 3, a cellular baseband unit (CLBB) 21, a digital/analog (D/A) converter 22, a reference frequency oscillator (VCXO) 23, and a transmission and reception antenna 24.

The baseband unit 21 engages in speech with a close 15 base station 5 or controls transfer of predetermined data through the transmission and reception antenna 24 in synchronization with the reference signal from the reference frequency oscillator 23. Further, the baseband unit 21 changes the oscillation frequency of the 20 reference frequency oscillator 23 according to established protocol if the base station 5 of the other party in communication changes by generating a frequency change signal S21 for reporting that the base station has changed and the frequency is to be changed and outputting 25 it to the D/A converter 22 and the control unit (CPU) of

the GPS baseband unit 4. When the baseband unit 21 outputs the frequency change signal S21 to the CPU of the GPS baseband unit 4, it uses a serial, IO, or interrupt function to output information of the reference frequency 5 changing, for example, the frequency change Δf or the DIR or number of bits or other parameters as the frequency change signal S21. Further, when receiving a frequency change stop signal S4 indicating that a positioning computation is in progress from the CPU of the GPS 10 baseband unit 4, the baseband unit 21 performs control so as to stop the output of the frequency change signal S21 to the D/A converter 22 and the CPU of the GPS baseband unit 4 and prevent the reference frequency oscillator 23 from changing the frequency until the state of the 15 positioning computation being in progress is lifted by the signal S4 even if the base station 5 of the other party in communication changes and it becomes necessary to change the oscillation frequency of the reference frequency oscillator 23 according to the established 20 protocols.

The D/A converter 22 converts a digital frequency change signal S21 from the baseband unit 21 to an analog signal and outputs it to the reference frequency oscillator 23.

25 The reference frequency oscillator 23 uses for

example 13 MHz \pm 0.1 ppm as the reference oscillation frequency f and supplies a reference signal Fox to the baseband unit 21 and the clock generation unit of the GPS baseband unit 4. The reference frequency oscillator 23 changes the oscillation frequency by exactly Δf (for example 0.7 Hz) according to the frequency change instruction of the analog frequency change signal S21 from the D/A converter 22 and supplies a reference signal Fox having a frequency $f+\Delta f$ after the change to the baseband unit 21 and the clock generation unit of the GPS baseband unit 4.

The GPS front end unit 3 receives a radio GPS signal RF having a high frequency 1575.42 MHz from the GPS satellite, amplifies the weak GPS signal, converts it to the intermediate frequency (IF) signal of 1.023 MHz, and further converts the analog IF signal to a digital IF signal and supplies the same to the GPS baseband unit 4.

The GPS front end unit 3 has, as shown in FIG. 3, an antenna 31, a low noise amplifier (LNA) 32, a band pass filter (BPF) 33 made of a SAW filter, an amplifier 34, a frequency synthesizer (FSYNS) 35, a mixer 36, an amplifier 37, a low pass filter (LPF) 38, and a analog/digital circuit (A/D) 39.

The frequency synthesizer 35 includes a PLL circuit etc., generates an oscillation signal S35 of for example

1574.393 MHz of 85.5 times the 18.414 MHz of the frequency FL0 of the reference clock CLK in accordance with the reference clock RCLK of 18.414 MHz generated at the GPS baseband unit 4 and the control signal of the CPU, 5 and supplies the same to the mixer 36.

The mixer 36 mixes the received RF signal of the frequency FRF (1575.42 MHz) and the frequency FL0 (1574.397 MHz) and converts the result to an IF signal S36 of a frequency FIF ($FRF \pm FL0 = 1.023$ MHz, 3139.817 MHz). 10

The LPF 38 outputs an IF signal S38 obtained by passing only the low frequency component of the IF signal S36 obtained at the mixer 36 via the amplifier 37, that is, the frequency FIF ($FRF - FL0 = 1.023$ MHz).

15 Note that, when the error of the reference clock RCLK is $\Delta FRCLK$ (= about ± 3 ppm), the frequency FL0 of the oscillation signal S35 of the frequency synthesizer 35 is given by the following equation:

$$FL0 = 121.10816 \times (13 \text{ MHz} + \Delta FRCLK)$$

20 Further, when the Doppler shift is ΔD , the frequency FIF of the IF signal S38 is given from the LPF 38 by the following equation:

$$FIF = 1.013962 \text{ MHz} + \Delta D + 121.10816 \times \Delta FRCLK$$

Note that the cycle T of the received C/A code does 25 not change according to the conversion of frequency from

the RF signal to the IF signal. Namely, this is irrelevant to the error ΔF_{RCLK} of the reference clock RCLK. The fluctuation of the cycle T is for example about (1 ms + change due to Doppler shift).

5 The GPS front end unit 3 receives the radio RF signal of the format as shown in FIG. 1 from the GPS satellite having a frequency of 1575.42 MHz at the antenna 31. The received RF signal is amplified at the low noise amplifier 32, is stripped of any signal outside 10 of the GPS signal band at the BPF 33 serving as the SAW filter, and is input to the mixer 36 via the amplifier 34. Then, the mixer 36 mixes it with the oscillation signal S35 by the frequency synthesizer 35, then passes the result through the amplifier 37 and the LPF 38 to extract 15 the IF signal S38 having a frequency of 1.023 MHz. The IF signal S38 is converted to a digital signal at the binary-coded circuit 39 and output as a one-bit serial signal IF signal S39 to the GPS baseband unit 4.

 The GPS baseband unit 4 receives the reference 20 signal Fox of the frequency 13 MHz (or ± 0.7 Hz) from the mobile phone unit 2, outputs a reference clock RCLK having a frequency of 13 MHz as it is or generates a clock obtained by multiplying or dividing the reference signal Fox, receives an IF signal S39 from the GPS front end unit 3 based on these clocks, performs acquisition 25

for finding the synchronization point initially or when the system largely deviates from the synchronized state, controls the delay difference to be sufficiently smaller than one chip length of the spread code after the 5 acquisition, performs tracking for establishing synchronization of the C/A code and carrier, and performs positioning computation, position retrieval, and other processing based on the range data, Doppler shift, navigation message, time, etc.

10 Further, the GPS baseband unit 4 receives the frequency change signal S21 from the mobile phone unit 2 and, based on the information of the reference frequency changing, for example, the frequency change Δf or the DIR or number of bits or other parameters, performs control 15 so as to continuously hold the synchronization of the IF carrier in the tracking processing.

Further, when the frequency of the reference signal Fox frequently changes, the GPS baseband unit 4 finds it difficult to synchronize with the frequency of the RF 20 signal from the GPS satellite found by the acquisition or find the frequency of the signal from the GPS satellite, therefore performs control to give the frequency change of the reference signal Fox to the carrier acquisition unit and the synchronization portion so as to synchronize 25 with the frequency of the signal from the GPS satellite

and reliably find the frequency of the signal from the GPS satellite. This control will be explained later.

Further, the GPS baseband unit 4 prevents the frequency of the IF carrier changing when the frequency 5 of the reference clock RCLK changes during the positioning computation and the synchronization of received signal and the spread code no longer being able to be held by outputting the frequency change stop signal S4 to the baseband unit 21 of the mobile phone unit 2 and 10 performing control so as to prevent a change of the frequency until the positioning computation is ended even if the base station 5 of the other party in communication changes and it becomes necessary to change the oscillation frequency of the reference frequency 15 oscillator 23 according to the established protocols. In a cellular system, the oscillation frequency is changed in 2 seconds or more, therefore the positioning computation may be ended during that time.

The GPS baseband unit 4 has, as shown in FIG. 3, a 20 clock generation unit (CLKGEN) 41, an oscillator (XO) 42, a real time clock unit (RTC) 43, a timer (TMR) 44, a memory unit (RAM/ROM) 45, an acquisition unit (ACQ) 46, a tracking unit (TRK) 47, and a control unit (CPU) 48.

The clock generation unit 41 includes a multiplier, 25 frequency divider, etc. Under the control of a control

signal S48 of a control unit 48, it receives the reference signal Fox using the 13 MHz ± 0.1 ppm (or ± 0.7 Hz) from the reference frequency oscillator 23 of the mobile phone unit 2 as the reference oscillation frequency f , generates the reference clock RCLK, and supplies the same to the frequency synthesizer 35 of the GPS front end unit 3. Further, the clock generation unit 41 generates the clock CLK obtained by multiplying or dividing the frequency 13 MHz ± 0.1 ppm (or ± 0.7 Hz) of 5 the reference signal Fox and supplies the same to the frequency synthesizer 35 of the GPS front end unit 3, the timer 44, the acquisition unit 46, and the tracking unit 47. Further, the clock generation unit 41 generates a sampling clock SCLK based on the reference signal Fox and 10 supplies the same to the acquisition unit 46. Further, the clock generation unit 41 generates operation clocks CLK etc. of the control unit 48, the timer 44, the memory unit 45, etc. based on the reference signal Fox. 15

The oscillator 42 generates a timer use clock CK 20 having a frequency of 32.768 kHz and supplies the same to the real time clock unit 43. The real time clock unit 43 receives the clock CK from the oscillator 42 and supplies a real time clock to the control unit 48.

There are a plurality of channels for transferring 25 signals concerning time with the timer 44 and the control

unit 48, for example, counting a reference clock RCLK having a frequency of for example 13 MHz. The plurality of channels include for example a channel used for a usual interval timer, a channel for a count of several 5 seconds or more or power management, and channels for other functions.

The memory unit 45 includes a RAM and ROM and is accessed by the control unit 48.

The acquisition unit 46 receives the IF signal from 10 the GPS front end unit 3 under the control of the control unit 48, performs a search of the GPS signal over a wide range (acquisition of the C/A code), processing for correlation detection by an FFT operation, and processing for removal of the navigation message, and transfers the 15 search result, the correlation detection result, and the C/A code phase, the carrier frequency, and correlation level to the control unit 48.

FIG. 4 is a block diagram of an example of the configuration of the acquisition unit 46 according to the 20 present embodiment.

The acquisition unit 46 has, as shown in FIG. 4, a serial/parallel converter (S/P) 461, a RAM 462, a DSP 463, and a memory (RAM/ROM) 464.

The serial/parallel converter 461 of the 25 acquisition unit 46 starts the sampling of the IF signal

(1 bit) based on the sampling clock SCLK by a command from the control unit 48, performs 4/18 thinning processing from the sample data of for example 13 MHz, converts the sampling signal to a parallel signal of 16 5 bits, and stores it in the RAM 462. Concretely, one dummy bit is inserted for every 1023 bits to obtain 4096 samples/ms.

The DSP 463 operates by a clock obtained by multiplying several fold (for example 3- or 4-fold) the 10 frequency 13 MHz ± 0.1 ppm (or ± 0.7 Hz) of the reference signal from the clock generation unit 41 and performs a search of the GPS signal with respect to the data stored in the RAM 462. Further, the DSP 463 utilizes an FFT to 15 detect the correlation with the C/A code so as to increase the speed. Further, the DSP 463 outputs an SV number, a C/A code phase np, a carrier frequency kc, and a correlation level to the control unit 48. Note that the resolution in the DSP 463 is for example 1/4 chip for the C/A code and (1/16) kHz for the carrier frequency.

20 FIG. 5 is a block diagram of an example of the configuration of the acquisition unit 46.

The DSP 463 has, as shown in FIG. 5, a pre-processing unit (PREPR) 4631, an FFT processing unit 4632, a memory shift unit (MSFT) 4633, a spread code generation 25 unit (PNG) 4634, an FFT processing unit 4635, a

multiplier 4636, an inverse FFT processing unit 4637, and a peak detection unit (PKDET) 4638.

The FFT processings of the FFT processing units 4632 and 4635 are basically carried out in for example 5 units of 16 ms. The pre-processing unit 4631 performs the pre-processing for reducing the amount of 16 ms (65536 points) to 4096 points in order to perform the FFT processing of the IF signal.

The result of the FFT processing of the FFT processing unit 4632 is input to the memory shift unit 4633 as the signal $R(k)$, subjected to shift processing by exactly k' , and input as the signal $R(k-k')$ to the multiplier 4636. Further, the C/A code $c(n)$ generated at the spread code generation unit 4634 is subjected to the 15 FFT processing at the FFT processing unit 4635. The result is input as a signal $C(k)$ to the multiplier 4636. The multiplier 4636 multiplies the output signal $R(k-k')$ of the memory shift unit 4633 and the output signal $C(k)$ of the FFT processing unit 4635 and inputs the result 20 $R(k-k') \cdot C(k)$ to the inverse FFT processing unit 4637. Then, a signal $f(n)$ obtained by the inverse FFT processing at the inverse FFT processing unit 4637 is input to the peak detection unit 4638, whereby the C/A code phase np , the carrier frequency kc , and the 25 correlation level are detected and output to the control

unit 48.

The DSP 463 performs processings as shown in FIG. 6 to FIG. 8 under the control of the control unit 48 when the GPS signal is searched for, concretely when the 5 frequency of the satellite is searched for.

The DSP 463 performs the frequency search by determining a clock stable area AST other than a margin area AMG set in advance in the area where the frequency of the reference signal Fox changes, that is, the clock 10 unstable area AUS as shown in FIG. 6, by the frequency change information of the reference signal Fox from the control unit 48 (the change interval of the frequency is 2 seconds or more), and using the IF data in the RAM 462 at that frequency. Namely, by using the certain time-wise 15 stable area for the frequency search of a satellite, it becomes possible to reliably find the frequency of the satellite. The positioning computation is also finished during this.

The DSP 463 receives the frequency change Δf of the 20 reference signal Fox from the control unit 48 and, as shown in FIG. 5, performs the frequency search of the satellite at the frequency before the frequency change of the reference signal Fox after the frequency change of the reference signal Fox. Namely, by using two or more 25 clock stable areas AST for the frequency search of the

satellite, it becomes possible to perform the frequency search of the satellite over a long time.

The DSP 463 establishes the synchronization of the frequency of the satellite by transmitting the change 5 $f + \Delta f$ to the tracking unit 47 when the frequency of the reference signal Fox changes after the frequency search of the satellite.

Further, the DSP 463 performs processing for removal of the navigation message. In the processing for 10 removal of this navigation message, if there is bit inversion of the navigation message in an interval of 16 ms, the correlation will not be constant. Therefore, for example, as shown in FIGS. 9A to 9D, the correlation value by the data sequence A having 16 cycles' length is 15 designated as Add(++) , the correlation value by the sequence B obtained by inverting the polarity of the latter half of A is designated as Add(+-) , and $|Add(++)| + |Add(+-)|$ is deemed constant. This is used as the correlation value. The bit transition position is 20 estimated from Add(++) and Add(+-) .

The tracking unit 47 has a delay locked loop (DLL) as the SS demodulator and a Costas loop as principal components. Under the control of the control unit 48, it receives the IF signal from the GPS front end unit 3, 25 performs processings such as tracking of the C/A code by

the DLL, tracking of the carrier by the Costas loop, and acquisition of the navigation message and the range data.

The tracking unit 47 has, for example as shown in FIG. 10, N (for example 12) number of DLL and Costas loop units (hereinafter, referred to as "loop units") 471-1 to 471-N connected in parallel with the input of the IF signal and a control register (CTLREG) 472 for transferring the control data with the control unit 48 and the synchronization holding unit 47 and transferring the control data etc. with the DLL & Costas loop units 471-1 to 471-N via the bus BS.

If the present frequency is f and the scalar multiple of the frequency change is Δfs , when the frequency of the reference signal F_{0X} from the mobile phone unit 2 does not change and a frequency change signal S_{21} is not received, that is, when there is no frequency change information, the tracking unit 47 is set with control information in the control register 472 by the control unit 48 and operates divided into the three groups of loop units shown in FIG. 11. Concretely, the tracking unit 47 operates divided into the group of loop units (471-1 to 471-N/3) for the currently synchronized frequency f and the groups of loop units (471-N/3+1 to 471-2N/3, 471-2N/3+1 to 471-N) for the frequency Δfs of the ± change from the currently synchronized frequency f

(scalar multiple of change). Due to this, when the frequency changes, it is possible to synchronize the loop units in either the \pm frequency band with respect to f and it is possible to continuously track a satellite.

5 When the frequency Δf_s (also \pm direction) after the change is known, it is not necessary to divide the loop units into a plurality of loop units.

If the present frequency is f and the scalar multiple of the frequency change is Δf_s , when the 10 frequency of the reference signal F_{0X} from the mobile phone unit 2 changes and the frequency change signal S_{21} is received, that is, when there is frequency change information, the tracking unit 47 is set with control information in the control register 472 by the control 15 unit 48 and operates divided into two groups of loop units as shown in FIG. 12. Concretely, the tracking unit 47 operates divided into the groups of loop units (471-1 to 471- $N/2$, 471- $N/2+1$ to 471- N) for the frequency Δf_s of the \pm change from the currently synchronized frequency f 20 excluding the currently synchronized frequency f band (scalar multiple of change). Due to this, when the frequency changes, it is possible to synchronize the loop units in either the \pm frequency band with respect to f , it is possible to continuously track the satellite, and 25 it is possible to reduce the number of channels. When the

frequency Δ fs after the change (also the direction of \pm) is known, it is not necessary to divide the loop units into a plurality of groups.

FIG. 13 is a circuit diagram of an example of the 5 concrete configuration of a loop unit 471 of the tracking unit 47 according to the present embodiment.

The loop unit 471, as shown in FIG. 13, comprises a Costas loop 500 for tracking of the carrier and the acquisition processing of the carrier frequency and the 10 navigation message and a DLL 600 for tracking of the C/A code and the acquisition processing of an epoch signal and range data connected in parallel with respect to the input of the IF signal.

The Costas loop 500 has multipliers 501 to 503, a 15 numerically controlled oscillator (NCO) 504, low pass filters (LPFs) 505 and 506, a phase detector (PD) 507, a loop filter 508, a correlation value operation unit 509, and a navigation message judgment unit 510.

In the Costas loop 500 and the DLL 600, the control 20 unit 48 sets SV, the C/A code phase, and the NCO frequency from the search results of the DSP 463 of the acquisition unit 46.

The LPFs 505 and 506 of the Costas loop 500 are 25 constituted by IIR filters 511 as shown in FIG. 14B modeled for example on the RC filter shown in FIG. 14A

and remove out-of-band noise of the BPSK signals.

Each IIR filter 511 is constituted by a left shift barrel shifter 5111, a right shift barrel shifter 5112, adders 5113 and 5114, and a register (REG) 5115 of predetermined bits (for example 22 bits). The output of the barrel shifter 5111 becomes $kX[n]$. The output of the barrel shifter 5112 becomes $kY[n-1]$. The output of the adder 5113 becomes $(1-k)Y[n-1]$. The output of the adder 5114 becomes $Y[n] = (1-k)Y[n-1]+kX[n]$. This $Y[n]$ is a differential approximation of the RC filter.

The phase detector 507 of the Costas loop 500 detects the phase difference of the carrier and the NCO 504 at for example a 1 ms interval, controls the NCO 504 via the loop filter 508 by the detected phase difference to perform the acquisition (frequency pull-in), and thereby performs the tracking processing. FIG. 15 shows the characteristics of the phase detector 507 of the Costas loop 500. The phase detector 507 has a good phase comparison characteristic not depending upon the signal intensity.

The loop filter 508 of the Costas loop 500 integrates the output (phase difference) of the phase comparator 507 and controls the NCO 504.

The loop filter 508 is constituted by for example a complete integration type active filter. For example, the

transmission function $F(s)$ of a complete integration type loop filter such as shown in FIGS. 16A and 16B can be represented as follows:

$$F(s) = (1+s\tau_2)/s\tau_1 = \tau_2/\tau_1 + 1/s\tau_1$$

5 $s\tau_1 = R_1C, s\tau_2 = R_2C$

When differentially approximated, this can be represented as follows:

$$Y[n] = Y[n-1] + a\{X[n] - X[n-1]\} + bX[n]$$

$$a = \tau_2/\tau_1, b = T/\tau_1$$

10 where, T is the sampling cycle (1 ms).

FIG. 16C shows a loop filter configured based on this equation. Here, "a" is the magnitude of the frequency difference, and "b" is the magnitude of the control with respect to the phase difference. A suitable 15 "a" and "b" are set from the pull-in range and noise tolerance. Further, the search result (frequency) of the DSP 463 of the acquisition unit 46 is made the initial value $Y[0]$.

In a Costas loop 500 having such a configuration, 20 the IF signal is multiplied by a prompt signal P at the multiplier 501, and the carrier synchronization carried out. The output signal of the multiplier 501 is input to the multipliers 502 and 503. The multiplier 502 is supplied with an in-phase signal I having a predetermined 25 frequency. The low frequency component of the

multiplication result is extracted at the LPF 505 and supplied to the phase detector 507, the correlation value operation unit 509, and the navigation message judgment unit 510. Further, the multiplier 503 is supplied with a 5 quadrature signal Q having a predetermined frequency. The low frequency component of the multiplication result is extracted at the LPF 506 and supplied to the phase detector 507 and the correlation value operation unit 509. Then, the detection result of the phase detector 507 is 10 fed back to the NCO 504 through the loop filter 508, and the carrier frequency acquisition (frequency pull-in) of the BPSK signal is carried out. Further, the correlation value operation unit 509 performs a calculation of (I^2+Q^2) to obtain the correlation value P and transfers 15 it to the control unit 48 via the control register 472. Further, the navigation message judgment unit 510 obtains the navigation message and transfers it to the control unit 48 via the control register 472.

Further, the DLL 600 has multipliers 601 to 606, 20 LPFs 607 to 610, correlation value operation units 611 and 612, a phase detector (PD) 613, a loop filter 614, a numerically controlled oscillator (NCO) 615, and a PN generator (PNG) 616.

The DLL 600 performs the synchronization processing 25 with the C/A code included in the IF signal. The PN

generator 616 independently computes the three correlation levels of a prompt or puncture signal P, an early signal E, and a late signal L and, as shown in FIGS. 17A to 17C, controls the phase so that the level 5 difference of E and L becomes the same (P is the maximum). Note that the start timing of the PN generator 616 detects the correlation with several chips before and after from the search results of the DSP 463 of the acquisition unit 46. Further, the initial value and the 10 limiter value of the NCO 615 are set based on the search results of the DSP 463 of the acquisition unit 46 by the control unit 48 via the control register 472.

The phase detector 613 of the DLL 600 detects a phase difference between the C/A code and the output of 15 the PN generator 616 at an interval of for example 20 ms. The NCO 615 is controlled by the detected phase difference via the loop filter 614 to perform the acquisition (phase pull-in) and thereby to perform the tracking processing. In the detection of the phase 20 difference, as I and Q, use is made of signals on the selected side.

The phase characteristics of the phase detector 613 are shown in FIGS. 18A and 18B. An example where the phase detector 613 shown in FIGS. 18A and 18B performs 25 the calculation by setting for example $(E-L)/(E+L)$ is

shown. FIG. 18A shows the characteristic in the case of ± 0.5 chip; and FIG. 18B shows the characteristic of a case of $\pm 4/18$ chip. In this way, the phase characteristic of the phase detector 613 has a good phase comparison 5 characteristic not depending upon the signal intensity.

The loop filter 614 of the DLL 600 can be configured in the same way as the loop filter 508 of the Costas loop 500 (FIG. 16C). Note that the sampling cycle is 20 ms.

10 In the DLL 600 having such a configuration, the IF signal is multiplied by the early signal E at the multiplier 601, and the output signal of the multiplier 601 is input to the multipliers 602 and 603. The multiplier 602 is supplied with the in-phase signal I 15 having a predetermined frequency. The low frequency component of the multiplication result is extracted at the LPF 607 and supplied to the correlation value operation unit 611. Further, the multiplier 603 is supplied with the quadrature signal Q having a 20 predetermined frequency. The low frequency component of the multiplication result is extracted at the LPF 608 and supplied to the correlation value operation unit 611. The correlation value operaiton unit 611 performs the 25 calculation of (I^2+Q^2) to obtain the correlation value E and supplies it to the phase comparator 613. Further, the

IF signal is multiplied by the late signal L at the multiplier 604, and the output signal of the multiplier 604 is input to the multipliers 605 and 606. The multiplier 605 is supplied with the in-phase signal I of the predetermined frequency. The low frequency component of the multiplication result is extracted at the LPF 609 and supplied to the correlation value operation unit 612. Further, the multiplier 606 is supplied with the quadrature signal Q having a predetermined frequency. The 10 low frequency component of the multiplication result is extracted at the LPF 620 and supplied to the correlation value operaiton unit 613. The correlation value operation unit 613 performs the calculation of (I^2+Q^2) to obtain the correlation value L and supplies it to the phase 15 comparator 613. Then, the phase detector 611 detects the phase difference between E and L, feeds back the detection result through the loop filter 614 to the NCO 615, and performs the acquisition (phase pull-in).

The control unit 48 basically performs the 20 processing as shown in FIG. 19. First, at step ST1, the control unit 48 selects the satellite. Concretely, according to the initial state of a cold start, a warm start, or a hot start, it determines the satellite for which synchronization is to be acquired and the algorithm, 25 controls the on/off state of the GPS front end unit 3,

adjusts the gain, and obtains the antenna connection information from the GPS front end unit 3. At step ST2, the control unit 48 controls the on/off state of the acquisition unit 46, transfers a program to the 5 acquisition unit 46, transfers the search command and SV information, and transfers various computation commands in accordance with the satellite for which synchronization is to be acquired and the algorithm, obtains the search results of the SV, the phase, the 10 frequency, and the level and various computation results from the acquisition unit 46, and sets the acquisition unit 46 to a standby state. At step ST3, the control unit 48 sets the search results and the computation results of the acquisition unit 46 in the tracking unit 47, controls 15 the on/off state for every channel of the tracking unit 47, controls the tracking, concretely, performs the initial settings, search, synchronization, and interpolation control, and obtains the range data, Doppler shift, navigation message, and time data from the 20 tracking unit 47. Then, at step ST4, the control unit 48 calculates the position and speed from the navigation message and the range data etc. and outputs the results according to the communication format.

Further, the control unit 48 receives the frequency 25 change signal S21 from the mobile phone unit 2 and

performs control so as to continuously maintain the tracking of the IF carrier in the tracking processing based on the information of the change of the reference frequency of the reference signal Foxno, for example, the 5 frequency change Δf or DIR or number of bits or other parameters. Further, when the frequency of the reference signal Fox is frequently changed, it becomes difficult to synchronize with the frequency of an RF signal from a GPS satellite found by the acquisition or to find the 10 frequency of a signal from a GPS satellite. Therefore, by giving the frequency change of the reference signal Fox to the acquisition unit and the synchronization unit, the control unit 48 performs control so as to synchronize 15 with the frequency of the signal from the GPS satellite and reliably find the frequency of the signal from the GPS satellite. Further, in order to prevent the frequency of the IF carrier also changing when the frequency of the reference clock RCLK changes during the positioning 20 computation and the synchronization of the received signal and the spread code no longer being able to be held, the control unit 48 outputs a frequency change stop signal S4 to the baseband unit 21 of the mobile phone unit 2 and performs control so as to prevent a change of the frequency until the positioning computation is ended 25 even if the base station 5 of the other party in

communication changes and it becomes necessary to change the oscillation frequency of the reference frequency oscillator 23 according to the established protocols. In a cellular system, the oscillation frequency is changed 5 in 2 seconds or more, therefore the positioning operation may be ended during that period.

FIG. 20 is a flow chart for briefly explaining the processing with respect to the DSP 463 of the acquisition unit 46 of the control unit 48.

10 At step ST11, the control unit 48 first selects a DSP program in accordance with the satellite for which synchronization is to be acquired and the algorithm. At step ST12, the control unit 48 sets the required command parameters such as the number of the satellite for which 15 synchronization is to be acquired. At step ST13, the control unit 48 releases the reset of the DSP 463 and activates the DSP 463. At step ST14, the control unit 48 reads the response after the end of the processing of the DSP 463. Then, the control unit 48 resets the DSP 463.

20 FIG. 21 is a flow chart for briefly explaining the processing with respect to the tracking unit 47 of the control unit 48.

At step ST21, the control unit 48 corrects the phase of PN changed in the time elapsed from when the DSP 25 463 fetched the data. At step ST22, the control unit 48

detects the peak while shifting the phase of the PN within a range of about 1 chip. At step ST23, the control unit 48 estimates the bit change point of the navigation message and determines the sampling timing of the 5 navigation message. At step ST24, the control unit 48 monitors and manages the state of the tracking. Then, at step ST25, the control unit 48 acquires the navigation message and the range data and hands it over to the position calculation routine.

10 Next, a brief explanation will be given of the search for the phase of the PN by the control unit 48. Even though the phase of the PN is corrected, as shown in FIGS. 22A to 22C, an error of about ± 0.5 chip is generated. For this reason, the phase of the PN is 15 searched for in the following way.

(1) The NCO 615 of the DLL 600 and the NCO 504 of the Costas loop 500 are set at values obtained at the acquisition unit 46, and the PN is reset with the phase of the PN shifted by -0.5 chip from the original phase. 20 At this time, the DLL 600 is turned off, and the NCO 615 of the DLL 600 and the NCO 504 of the Costas loop 500 are not updated.

(2) It is confirmed at that point of time if the correlation can be obtained.

25 (3) The NCO 615 of the DLL 600 is set at a value

advanced by exactly +3/18 chip at 20 ms, and the correlation after 20 ms is confirmed.

(4) The processing of (3) is repeated.

By the above search, when the value is higher than 5 the determined threshold value, it is regarded that the correlation was detected, the DLL 600 is turned on, and the feedback control of the NCO 615 of the DLL 600 and the NCO 504 of the Costas loop 500 is started.

Further, as the processing concerning the NCO 615 10 of the DLL 600, the control unit 48 calculates a mean value of the NCO 615 and sets the NCO limiter based on that value. For example, the control unit 48 updates the mean value at every 20 ms and sets the limiter value for every second to ± 8 of the mean value. As the processing 15 concerning the NCO 504 of the Costas loop 500, the control unit 48 calculates the mean value of the NCO 504 and sets the NCO limiter based on that value. For example, the control unit 48 updates the mean value at every 20 ms 20 and sets the limiter value for every second to ± 25 Hz of the mean value.

Further, FIGS. 23A and 23B are views for explaining the decoding function of the navigation message (one word) of the control unit 48.

The control unit 48 decodes the navigation message 25 as shown in FIG. 23A. Next, the control unit 48 executes

a parity check. The PE bit becomes "1" at the time of a parity error. Further, the control unit 48 executes a preamble check. The PR becomes "1" when the parity is OK and d1 to d8 are 0 x 8b. Note that when for example the 5 preamble is found, an interruption is caused.

Next, an explanation will be given of the operation centered on the portion concerning the frequency change information of the reference signal Fox.

The baseband unit 21 of the mobile phone unit 2 10 engages in speech with a close base station 5 or controls the transfer of predetermined data through the transmission and reception antenna 24 in synchronization with the reference signal from the reference frequency oscillator 23. The baseband unit 21 changes the 15 oscillation frequency of the reference frequency oscillator 23 according to the established protocols when the base station 5 of the other party in communication changes by generating a frequency change signal S21 for reporting that the base station has changed and the 20 frequency is to be changed and outputting it to the D/A converter 22 and the control unit 48 of the GPS baseband unit 4. At this time, the baseband unit 21 uses a serial, IO, or interrupt function to output information of the reference frequency changing, for example, the frequency 25 change Δf or DIR or number of bits or other parameters as

the frequency change signal S21.

The D/A converter 22 converts the digital frequency change signal S21 from the baseband unit 21 to an analog signal and outputs it to the reference frequency oscillator 23. The reference frequency oscillator 23 supplies the reference signal F_{ox} using for example 13 MHz ± 0.1 ppm as the reference oscillation frequency f to the baseband unit 21 and the clock generation unit 41 of the GPS baseband unit 4. The reference frequency oscillator 23 changes the oscillation frequency by exactly Δf (for example 0.7 Hz) according to the frequency change instruction of the analog frequency change signal S21 from the D/A converter 22 and supplies the reference signal F_{ox} of frequency $f + \Delta f$ after the change to the baseband unit 21 and the clock generation unit 41 of the GPS baseband unit 4.

Here, for example, the control unit 48 of the GPS baseband unit 4 determines the satellite for which synchronization is to be acquired and the algorithm in accordance with the initial state of the cold state, warm state, or hot state, controls the on/off state of the GPS front end unit 3, adjusts the gain, etc.

The GPS front end unit 3 receives the radio RF signal from the GPS satellite having a frequency of 25 1575.42 MHz at the antenna 101. The received RF signal is

amplified at the low noise amplifier 32, stripped of any signals out of the GPS signal band at the BPF 33 serving as the SAW filter, and input via the amplifier 34 to the mixer 36. Then, the mixer 36 mixes it with the 5 oscillation signal S35 from the frequency synthesizer 35. An IF signal S38 having a frequency of 1.023 MHz is extracted through the amplifier 37 and the LPF 38. The IF signal S38 is converted to a digital signal at the binary-coded circuit 39 and output as the one-bit serial 10 signal IF signal S39 to the GPS baseband unit 4.

The GPS baseband unit 4 receives the reference signal Fox having a frequency of 13 MHz (or ± 0.7 Hz) from the mobile phone unit 2 and outputs a reference clock RCLK of the frequency 13 MHz as it is or generates a 15 clock obtained by multiplying or dividing the reference signal Fox. Then, based on these clocks, it receives the IF signal S39 from the GPS front end unit 3, performs acquisition for finding the synchronization point initially or when the system largely deviates from the 20 synchronized state, controls the delay difference to be sufficiently smaller in value than one chip length of the spread code after the acquisition, and performs tracking for establishing synchronization of the C/A code and carrier.

25 The DSP 463 of the acquisition unit 46 uses the

frequency change information of the reference signal Fox from the control unit 48 to determine a clock stable area AST other than a margin area AMG set in advance in the area where the frequency of the reference signal Fox 5 changes, that is, the clock unstable area AUS, and uses the IF data in the RAM 462 at that frequency for a frequency search. Namely, the DSP 463 is controlled so as to reliably find the frequency of a satellite by the control unit 48 using the certain time-wise stable area 10 for the frequency search of a satellite. Further, the DSP 463 receives the frequency change Δf of the reference signal Fox from the control unit 48 and searches for the frequency of the satellite at the frequency before the frequency change of the reference signal Fox after the 15 frequency change of the reference signal Fox. Namely, the DSP 463 is controlled so as to perform the frequency search of the satellite over a long time by the control unit 48 using two or more clock stable areas AST for the frequency search. When the frequency of the reference 20 signal Fox changes after the frequency search of a satellite, the DSP 463 establishes synchronization of frequency of the satellite by transmitting the change $f+\Delta f$ to the tracking unit 47.

The control unit 48 of the GPS baseband unit 4 receives the frequency change signal S21 from the mobile 25

phone unit 2 and, based on the information that the reference frequency changes, for example, the frequency change Δf or DIR or the number of bits or other parameters, performs control so as to continuously maintain the tracking of the IF carrier in the tracking processing. For example, when the frequency of the reference signal F_{0X} of the mobile phone unit 2 does not change, the control unit 48 does not receive the frequency change signal S21, and there is no frequency change information, the tracking unit 47 is set with control information in the control register 472 by the control unit 48 and controlled so as to operate divided into the group of loop units (471-1 to 471-N/3) for the currently synchronized frequency f and the groups of loop units (471-N/3+1 to 471-2N/3, 471-2N/3+1 to 471-N) for the frequency Δf_s of the \pm change from the currently synchronized frequency f (scalar multiple of change). Due to this, where the frequency changes, it is possible to synchronize the loop units in either the \pm frequency band with respect to f , and it is possible to continuously track the satellite. When the change of the frequency including also \pm is known, the loop units do not need to be divided into a plurality of groups.

Further, when the frequency of the reference signal F_{0X} from the mobile phone unit 2 changes, the control

unit 48 receives a frequency change signal S21, and there is frequency change information, the tracking unit 47 is set with control information in the control register 472 by the control unit 48 and is controlled to operate 5 divided into the groups of loop units (471-1 to 471-N/2, 471-N/2+1 to 471-N) for the frequency Δf_s of the \pm change from the currently synchronized frequency f excluding the currently synchronized frequency f band (scalar multiple of change). Due to this, when the frequency changes, it 10 is possible to synchronize the loop units in either the \pm frequency band with respect to f , it is possible to continuously track the satellite, and it is possible to reduce the number of channels.

The control unit 48 performs the processings of the 15 positioning computation, the position search, etc. based on the range data, the Doppler shift, the navigation message, and the time obtained by the above tracking processing. The control unit 48 prevents the frequency of the IF carrier changing when the frequency of the 20 reference clock RCLK changes and the synchronization of the received signal and the spread code no longer being able to be held during the positioning computation by outputting the frequency change stop signal S4 to the baseband unit 21 of the mobile phone unit 2. Due to this, 25 the mobile phone unit 2 is controlled so as not to change

the frequency until the positioning operation is ended even if the base station 5 of the other party in communication changes and it becomes necessary to changing the oscillation frequency of the reference 5 frequency oscillator 23 according to the established protocols. In a cellular system, the oscillation frequency is changed in 2 seconds or more, therefore the positioning computation may be ended during that time.

As explained above, according to the present 10 embodiment, in order to change the oscillation frequency of the reference frequency oscillator 23 according to the established protocols if the base station 5 of the other party in communication changes, a frequency change signal S21 for reporting that the base station has changed and 15 the frequency is to be changed is generated and output to the control unit 48 of the GPS baseband unit 4. When not receiving the frequency change signal S21, the control unit 48 causes the tracking unit 47 to operate divided into three groups of loop units of the group of loop 20 units for the currently synchronized frequency f and groups of loop units for the frequency $f \pm \Delta f_s$ of the \pm change, while when receiving the frequency change signal S21, the control unit 48 causes the tracking unit 47 to operate divided into the two groups of loop units of the 25 groups of loop units for the frequency $f \pm \Delta f_s$ of the \pm

change excluding the currently synchronized frequency f band. Therefore, there are the advantages that, when the frequency changes, it is possible to synchronize the loop units in either the \pm frequency band with respect to f , 5 it is possible to continuously track a satellite, and it is possible to reduce the number of channels. Namely, according to the present embodiment, not only can an increase of the module size and cost be prevented, but also, even if the oscillation frequency of the reference 10 frequency oscillator changes, the synchronization of the frequency with the satellite can be established, and the tracking of the IF carrier can be continuously held.

Further, the control unit 48 performs control so that, during the positioning computation, the frequency 15 change stop signal $S4$ is output to the baseband unit 21 of the mobile phone unit 2 and a change of the frequency change is prevented until the positioning computation is ended even if the base station 5 of the other party in communication changes and it becomes necessary to change 20 the oscillation frequency of the reference frequency oscillator 23 according to the established protocols. Therefore, the frequency of the IF carrier changing when the frequency of the reference clock $RCLK$ changes and the synchronization of the received signal and the spread 25 code no longer being able to be held can be prevented.

Further, according to the present embodiment, at the DSP 463 of the acquisition unit 46, by using a certain time-wise stable area for the frequency search of a satellite, it becomes possible to reliably find the 5 frequency of a satellite. Further, at the DSP 463, by receiving the frequency change Δf of the reference signal Fox from the control unit 48, performing a frequency search of a satellite at the frequency before the frequency change of the reference signal Fox after the 10 frequency change of the reference signal Fox, and using two or more clock stable regions AST for the frequency search of the satellite, it is possible to perform the frequency search of the satellite over a long time. Further, when the frequency of the reference signal Fox 15 changes after the frequency search of the satellite, the DSP 463 can establish synchronization of the frequency with the satellite by transmitting the change $f+\Delta f$ to the tracking unit 47.

Summarizing the effects of the invention, as 20 explained above, according to the present invention, there are the advantages that not only can increases of the module size and cost be prevented, but also, even if the oscillation frequency of the reference frequency oscillator changes, synchronization of the frequency with 25 a satellite can be established and tracking of the

carrier can be continuously maintained.

While the invention has been described with reference to specific embodiments chosen for purpose of illustration, it should be apparent that numerous 5 modifications could be made thereto by those techniqueed in the art without departing from the basic concept and scope of the invention.